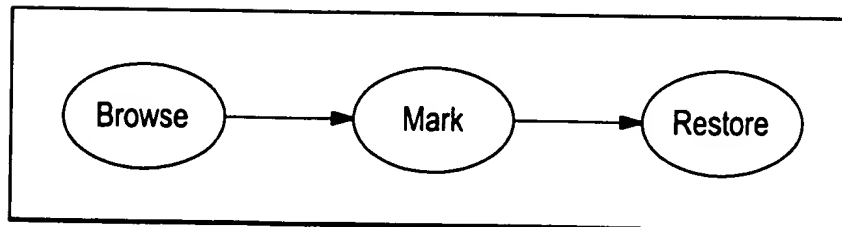


PRIOR ART

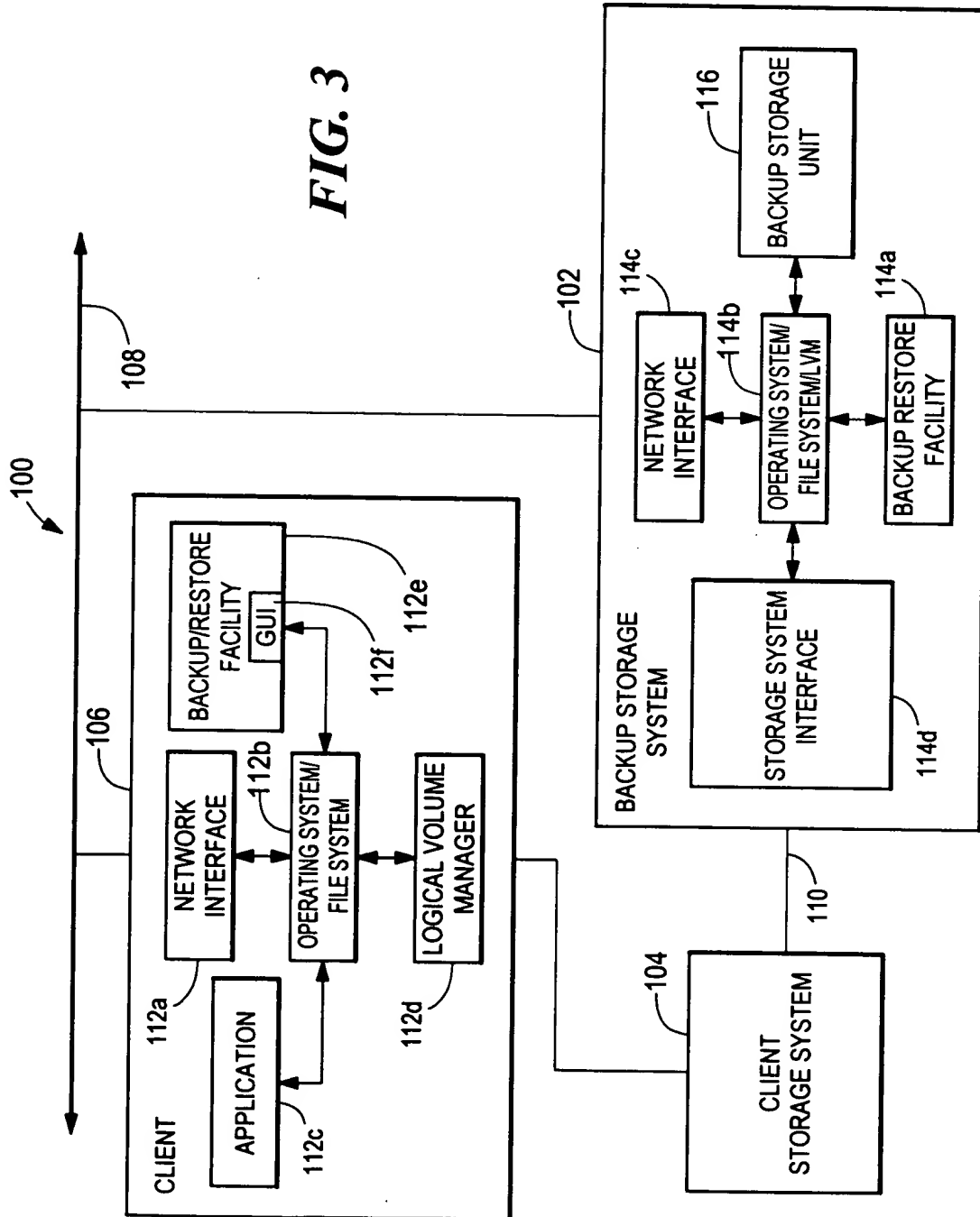
**FIG. 1**



PRIOR ART

**FIG. 2**

FIG. 3



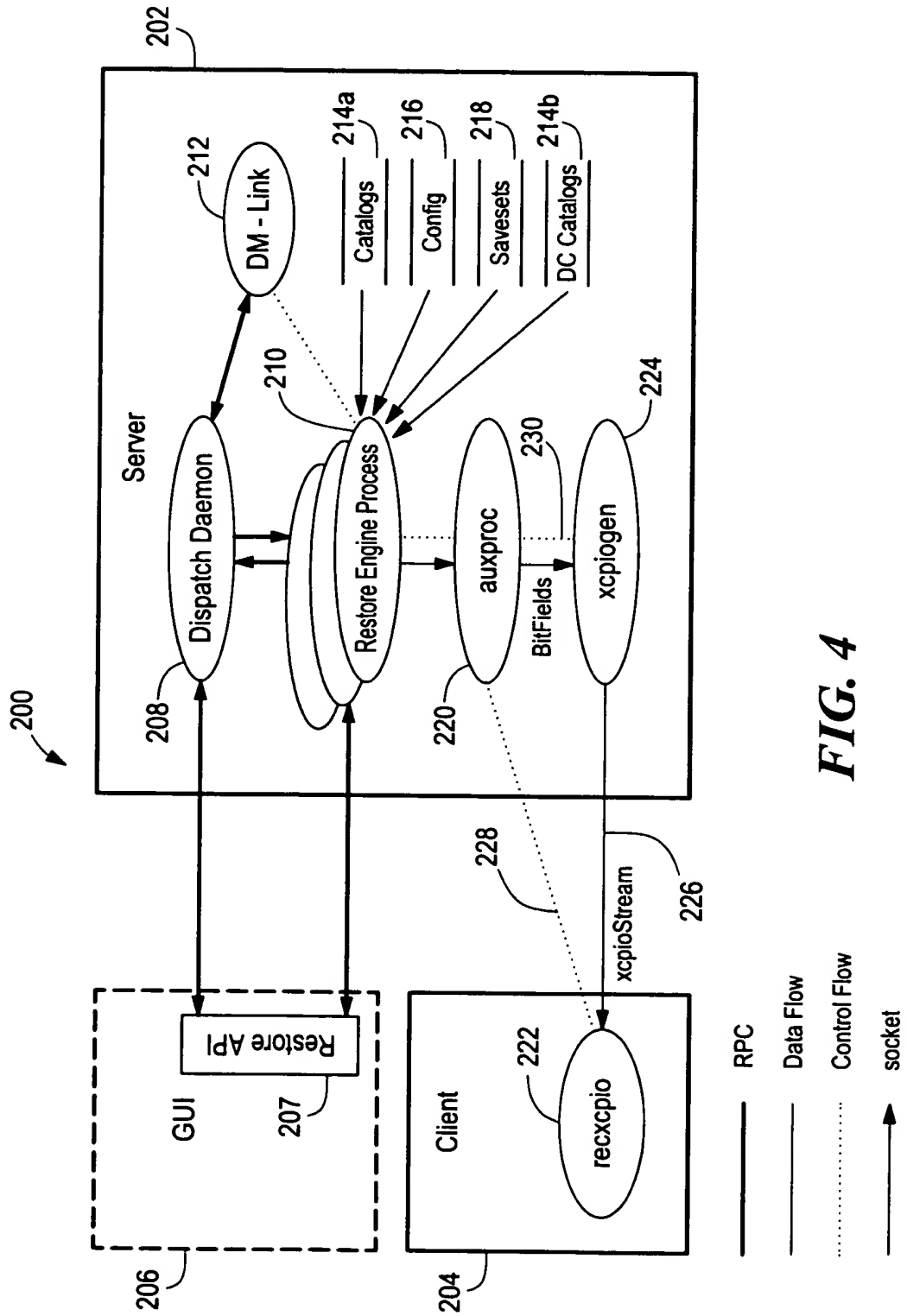


FIG. 4

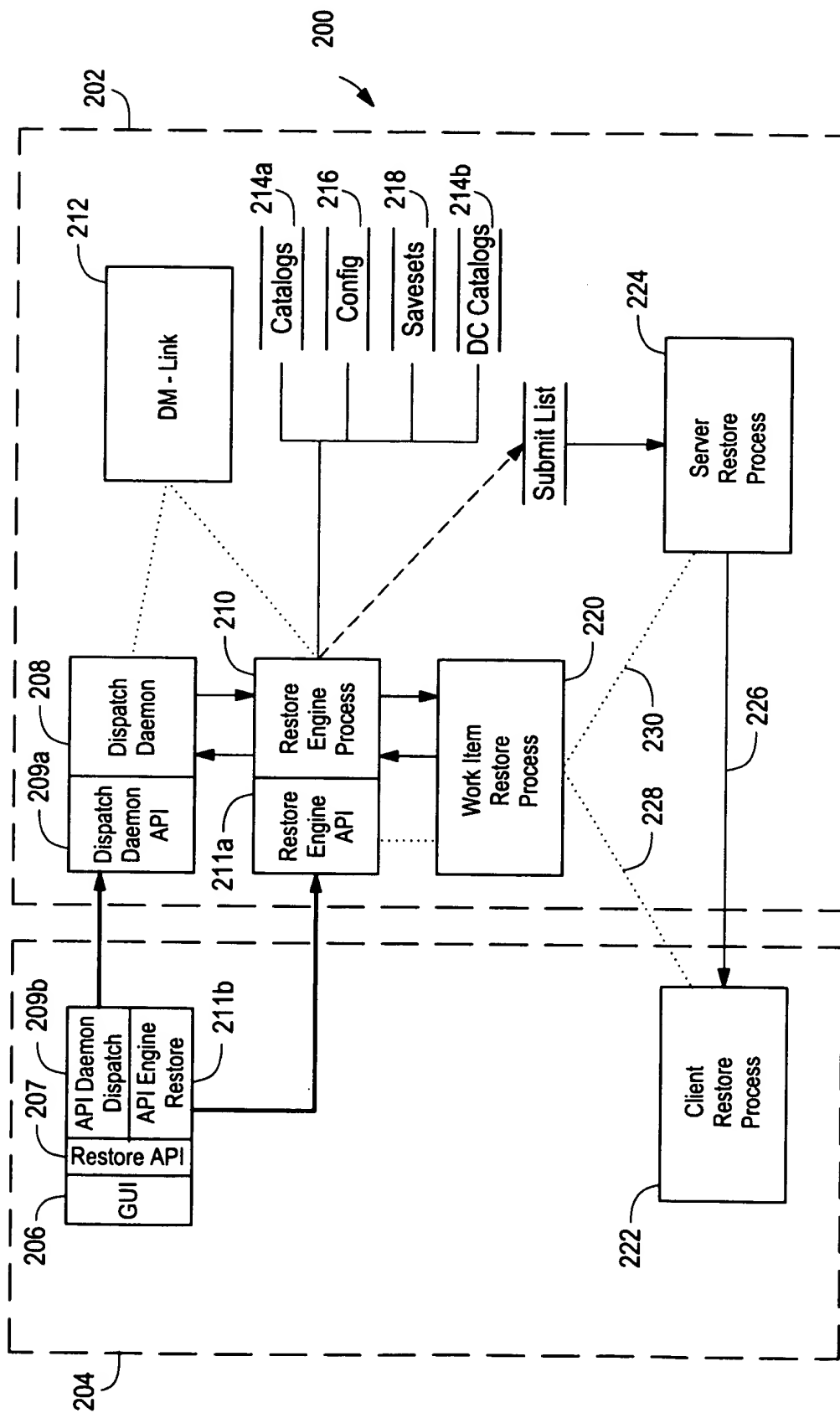
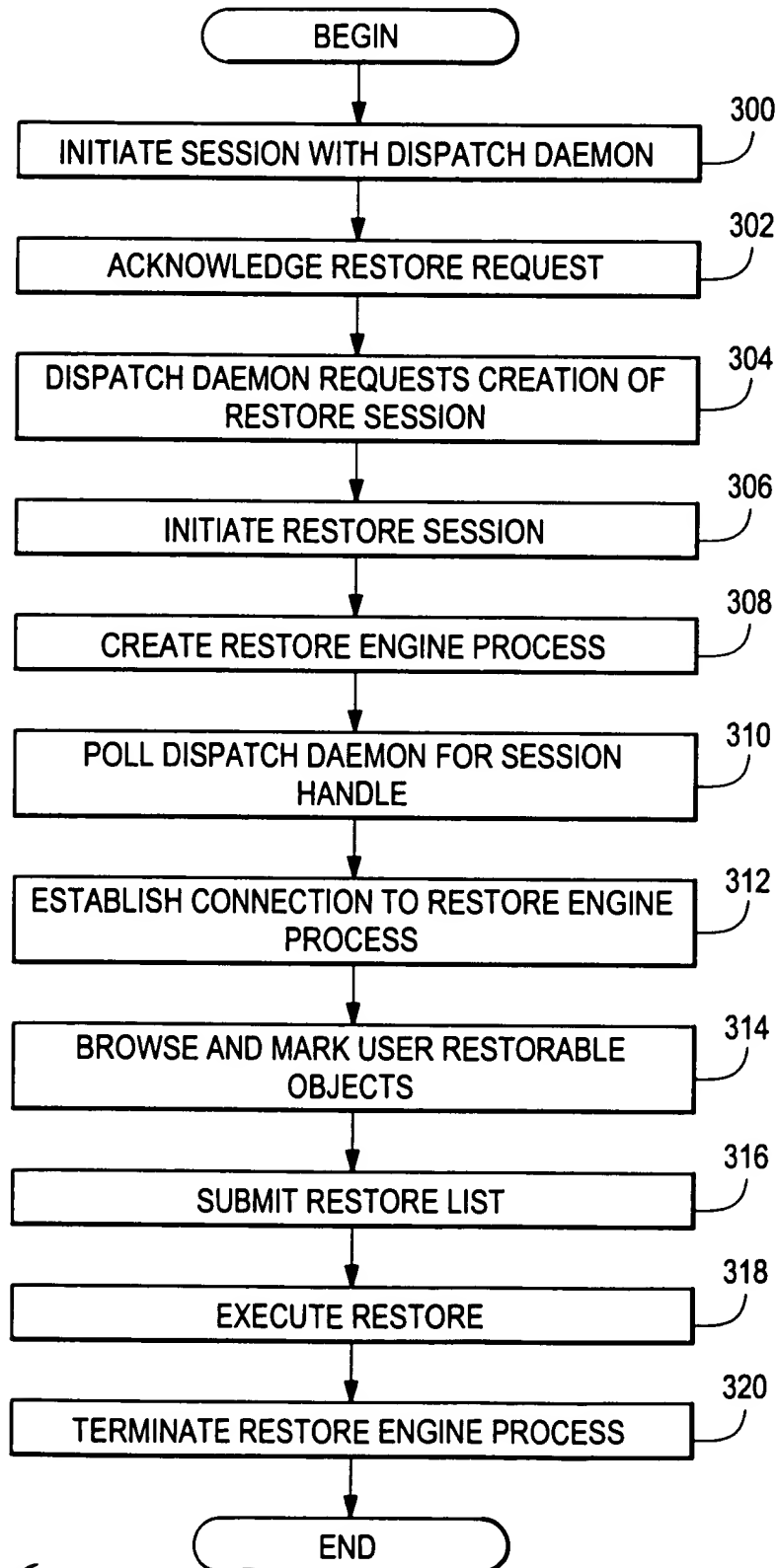
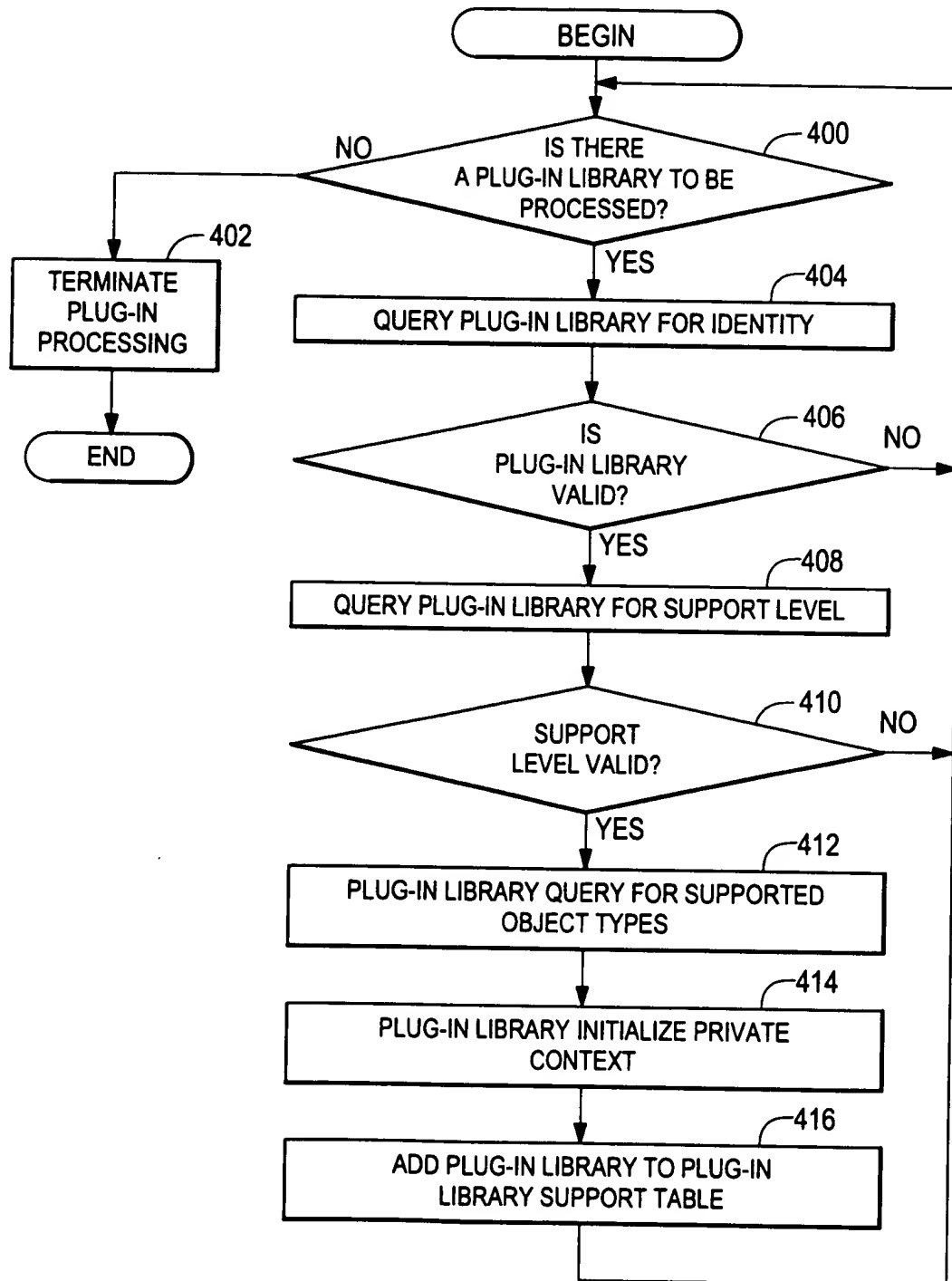


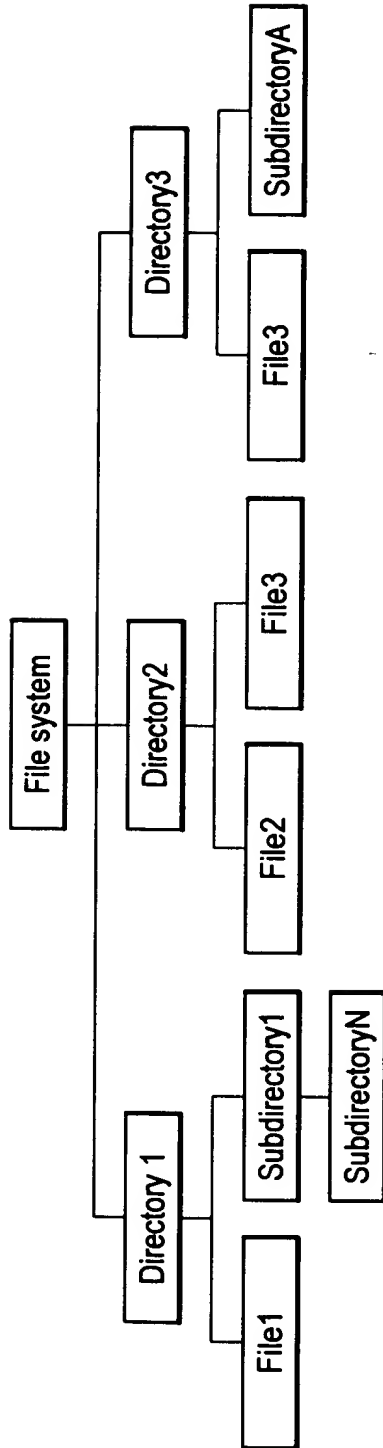
FIG. 5



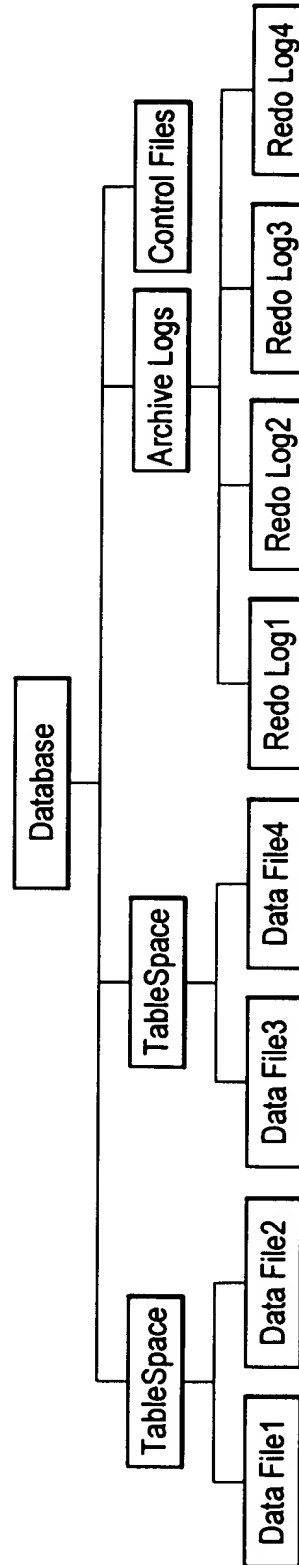
**FIG. 6**



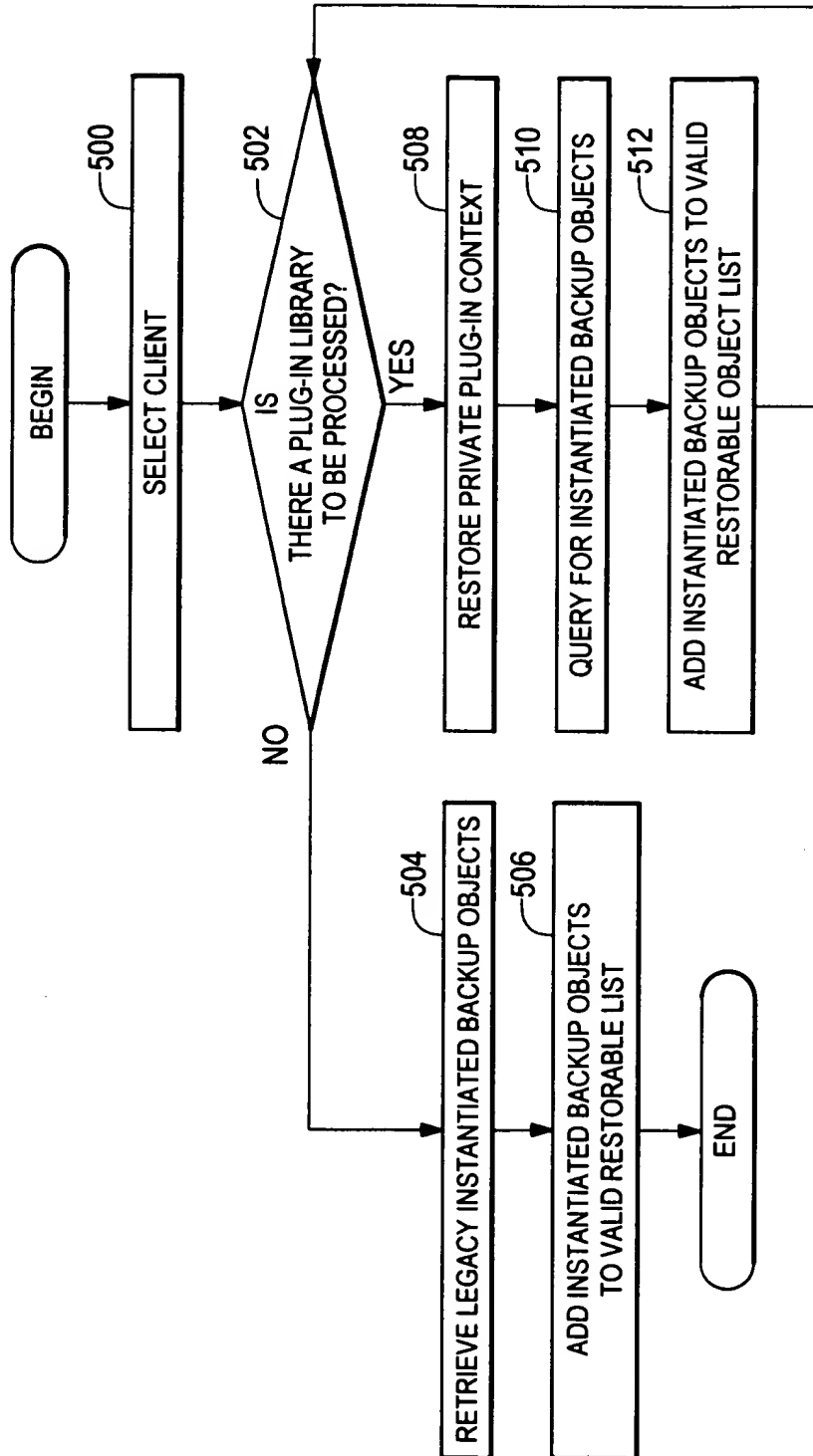
**FIG. 7**



**FIG. 7A**

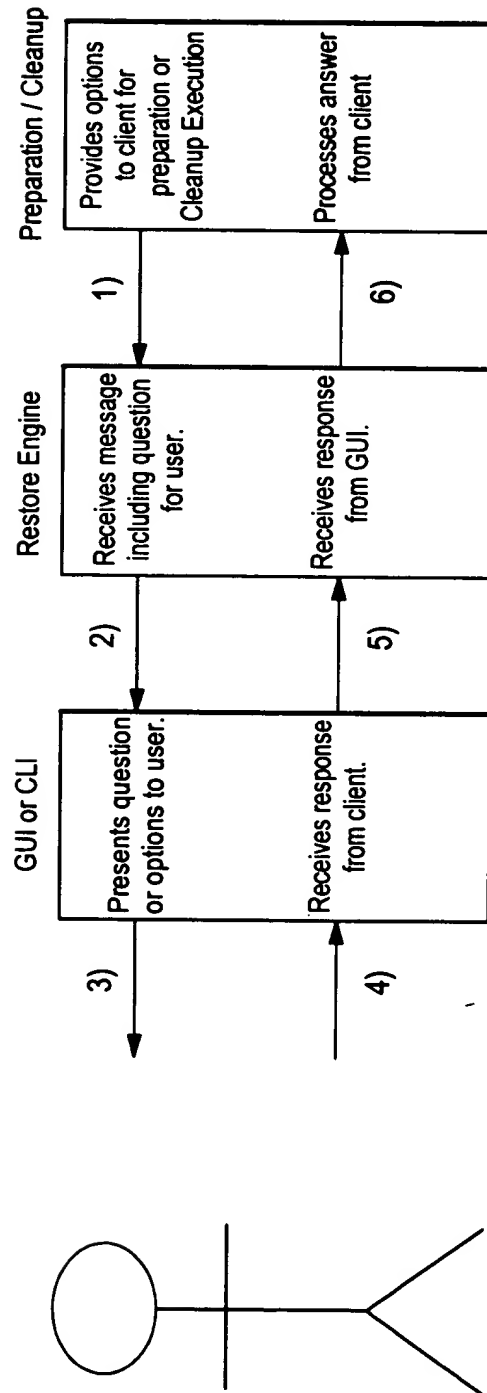


**FIG. 7B**

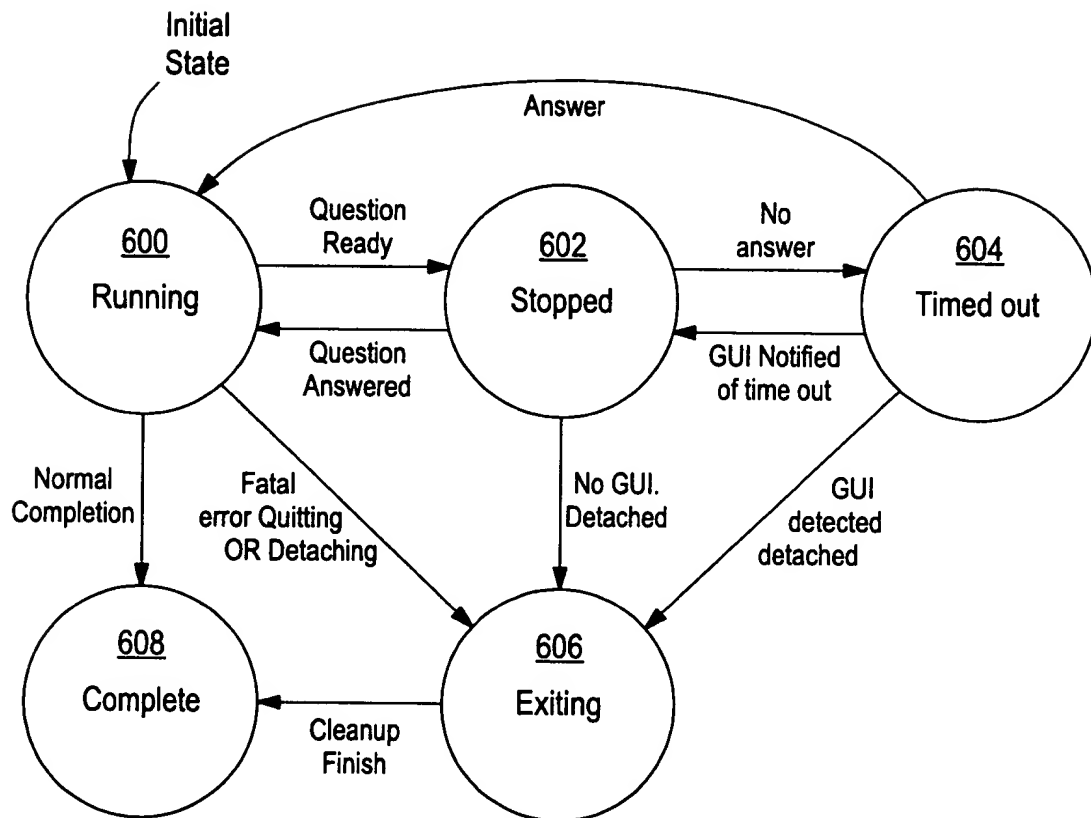


**FIG. 8**

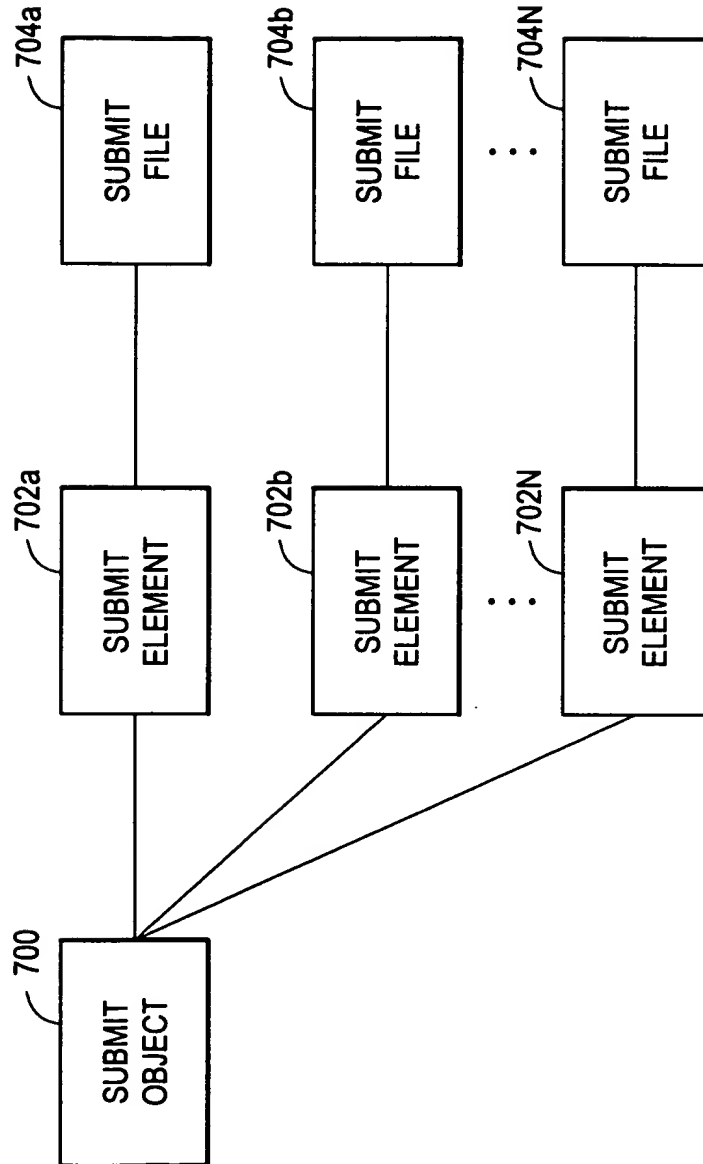




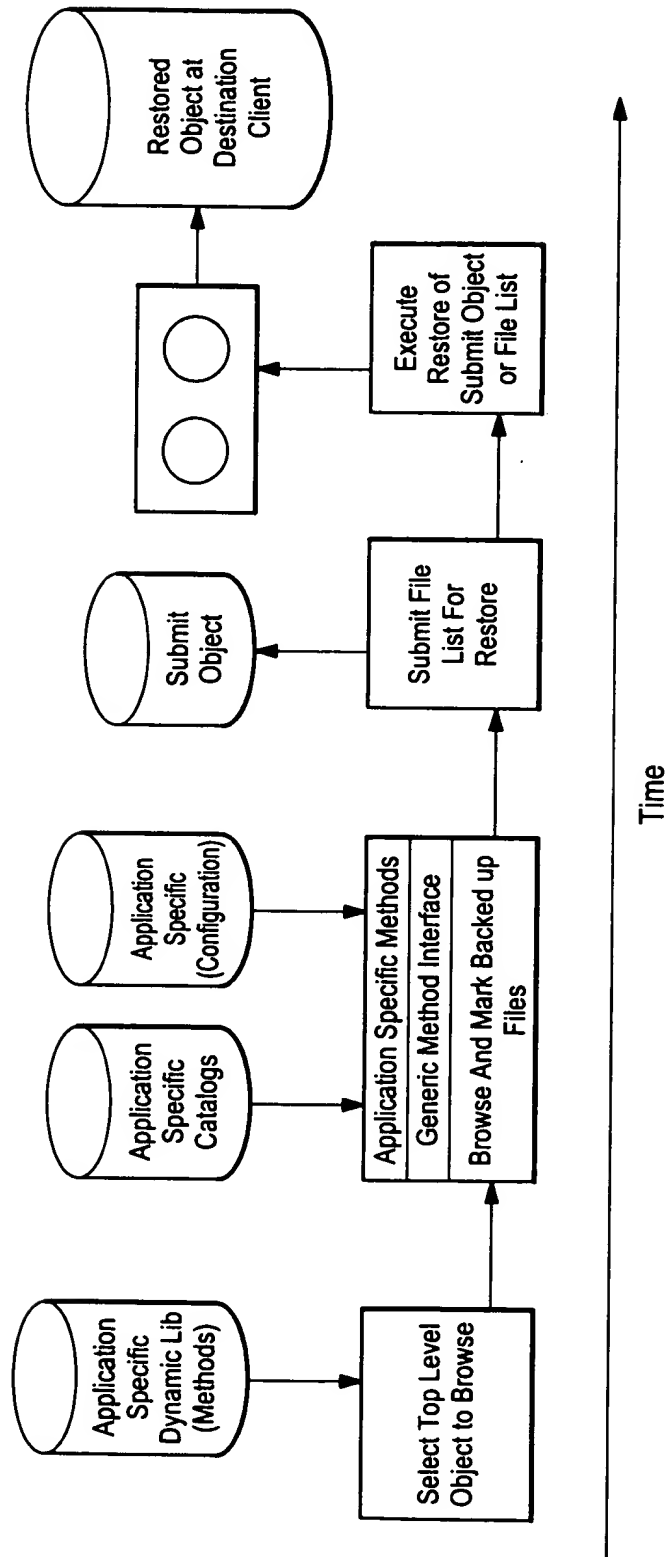
**FIG. 8A**



**FIG. 9**



**FIG. 10**



**FIG. 10A**

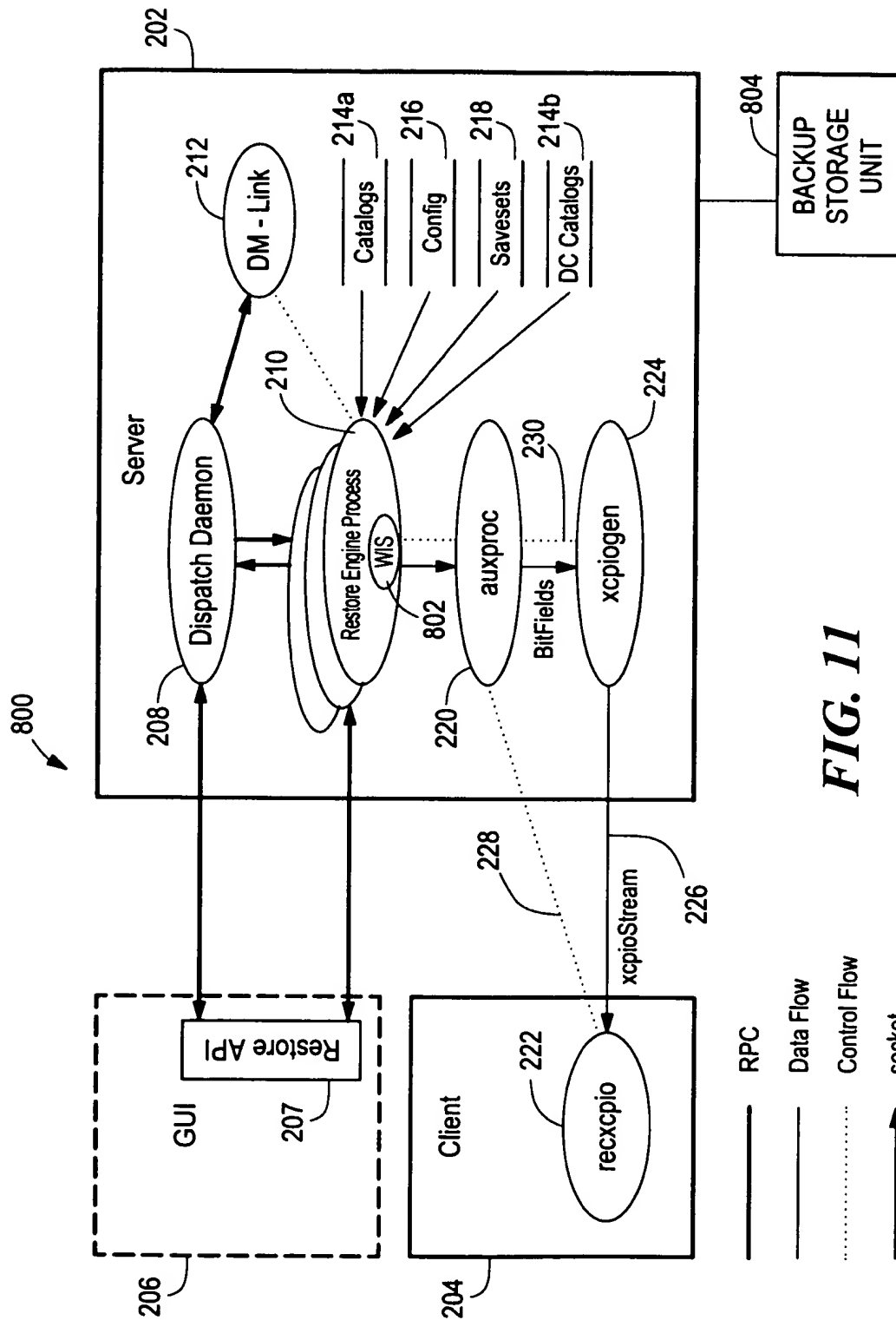
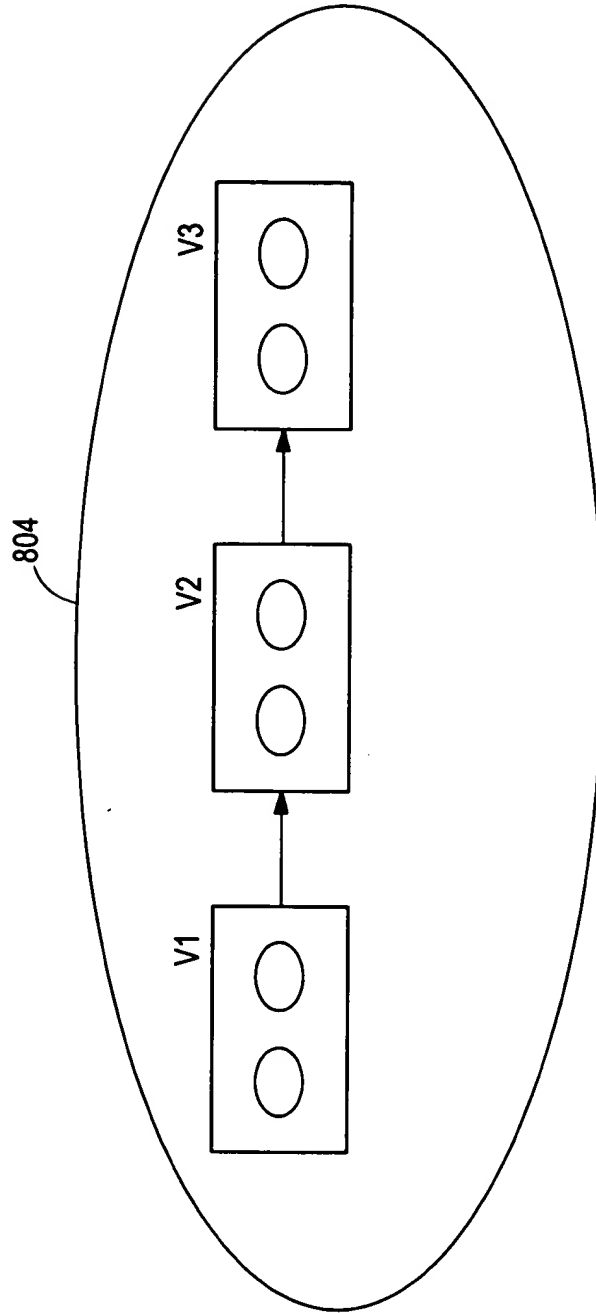


FIG. 11



**FIG. 12**

"FIG. 12" 11/23/2000

FIG. 13

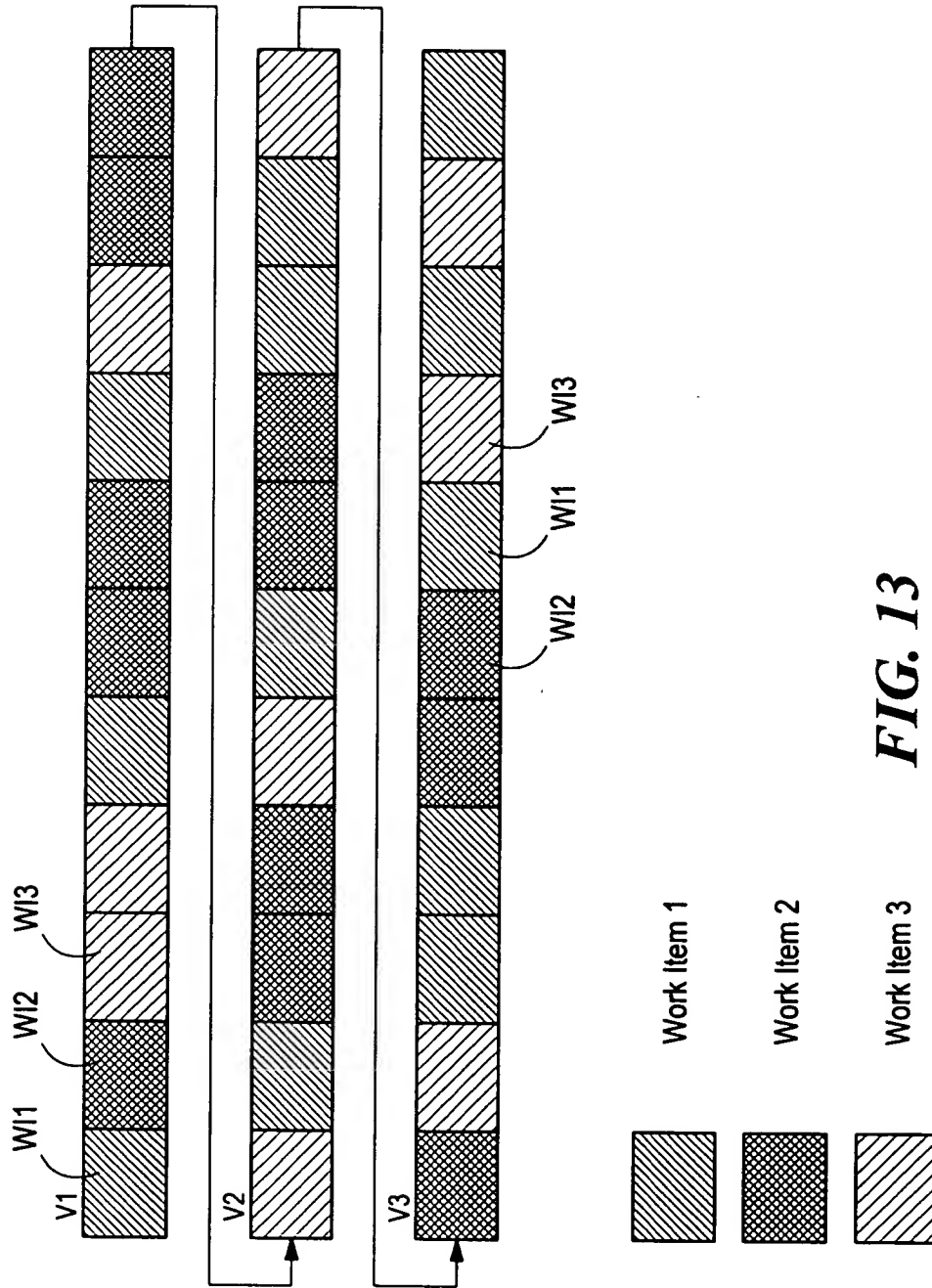
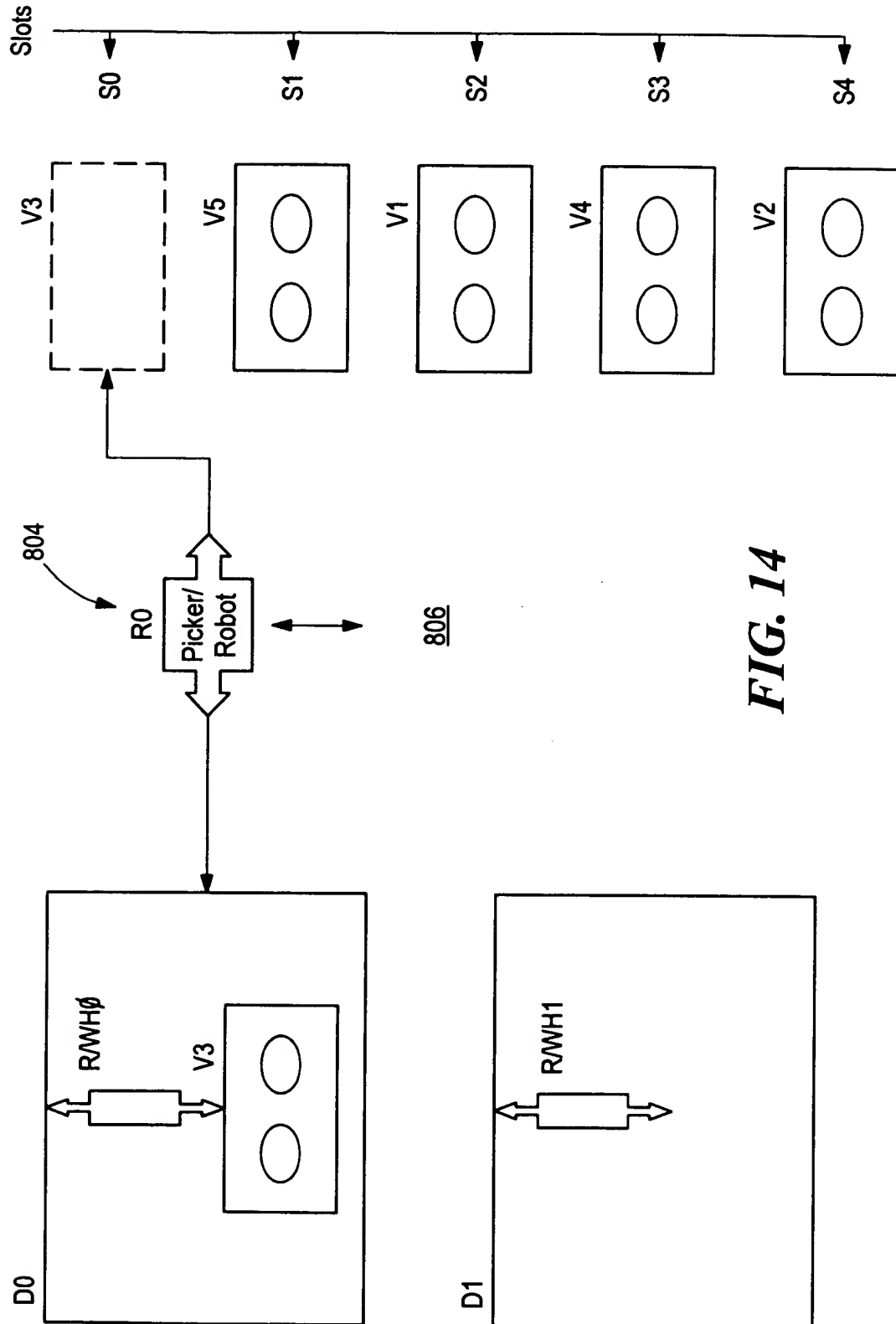
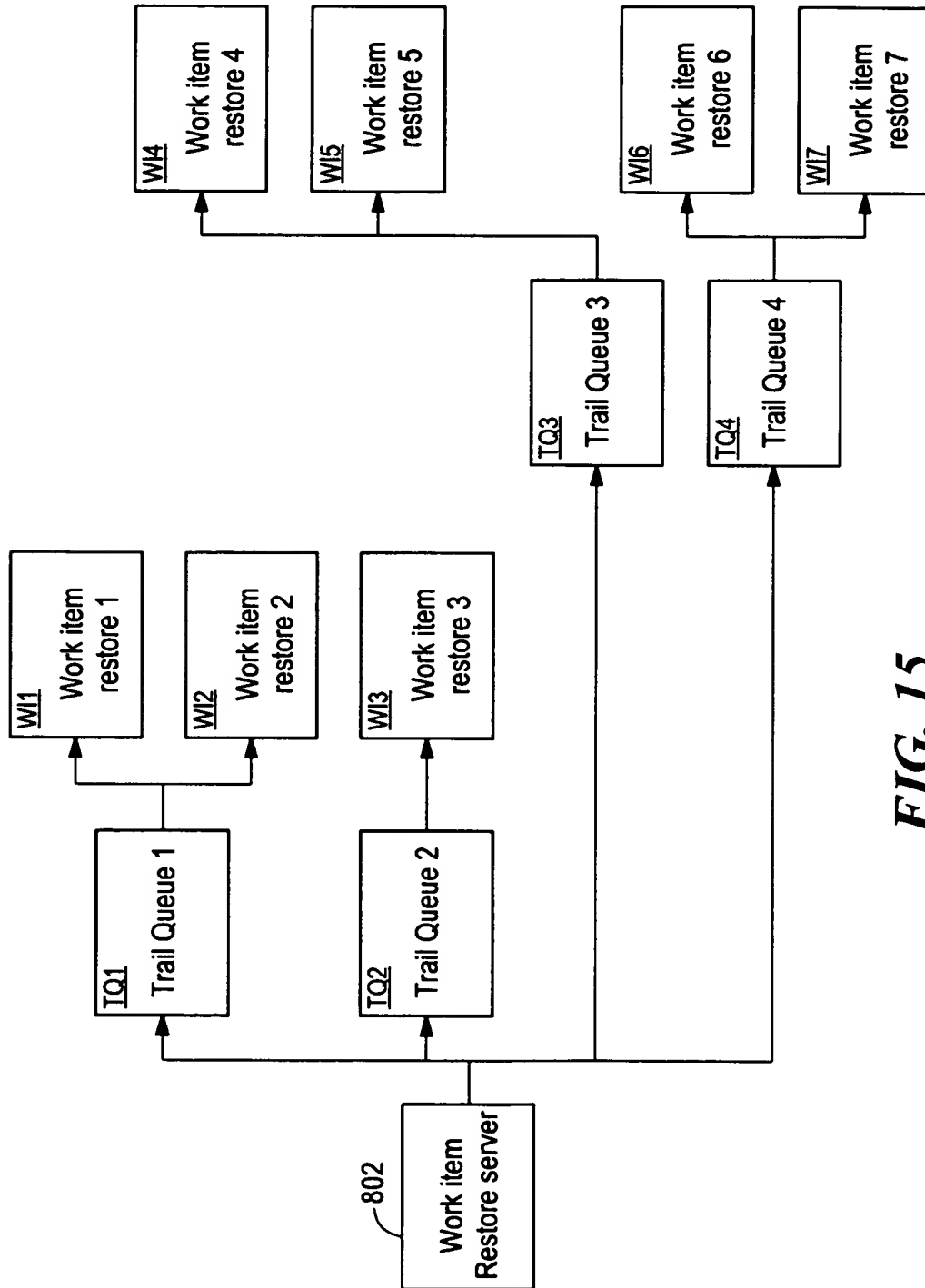


FIG. 13



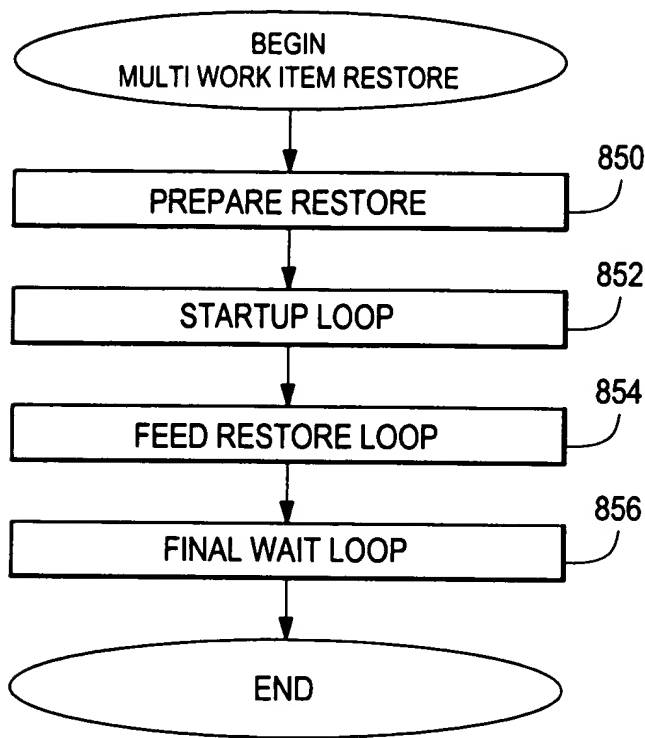




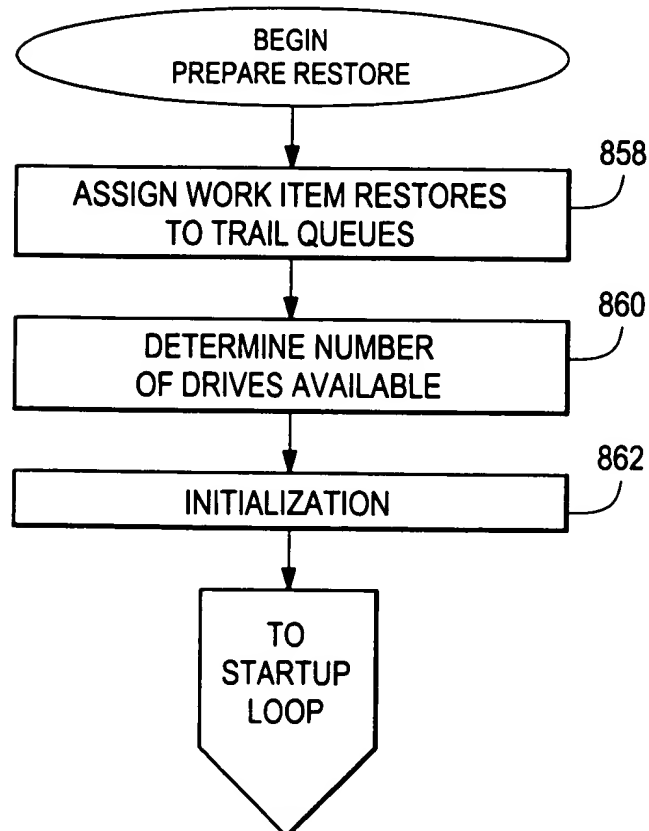
**FIG. 15**

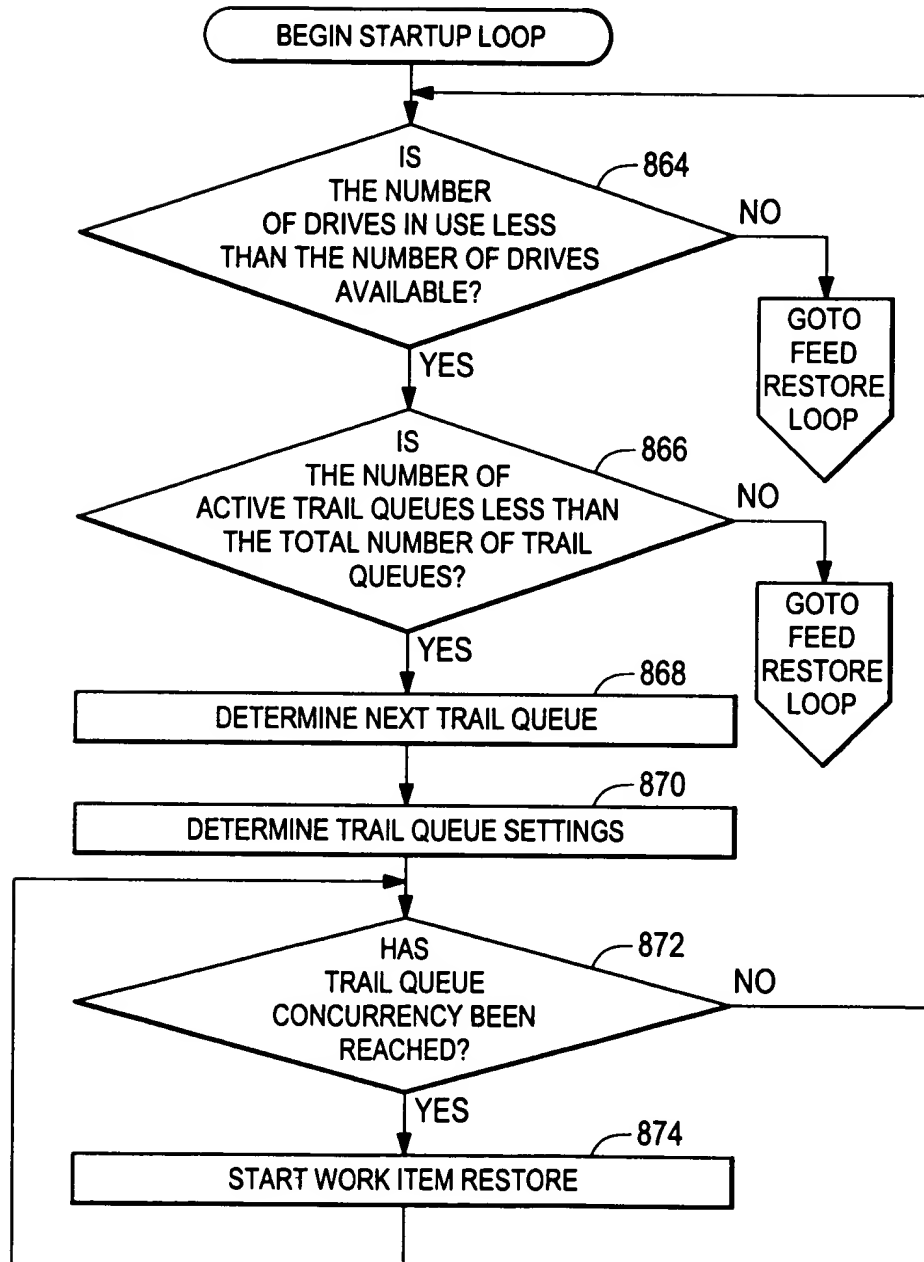
FIG. 15 is a block diagram of a restore architecture.

**FIG. 16**

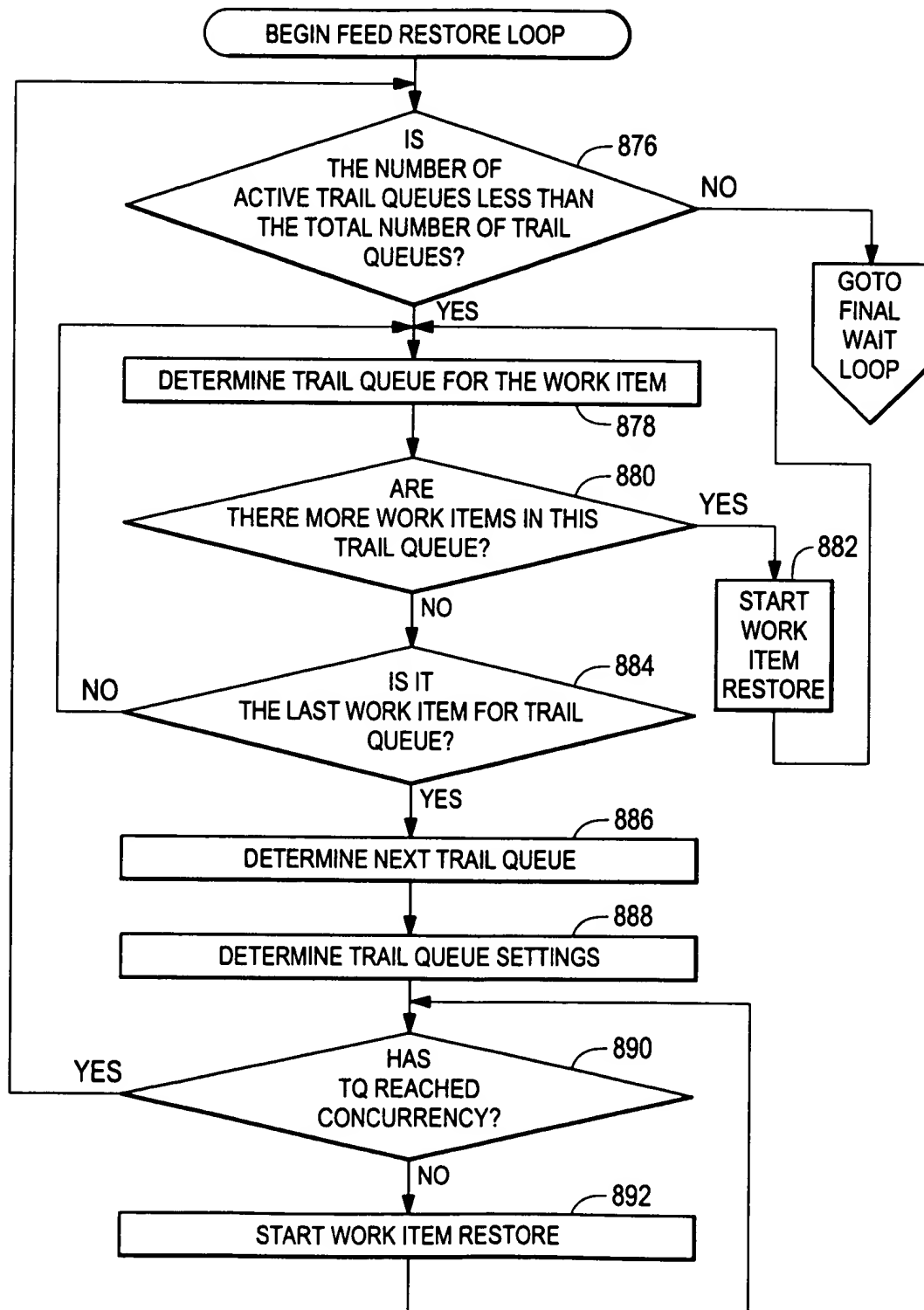


**FIG. 17**

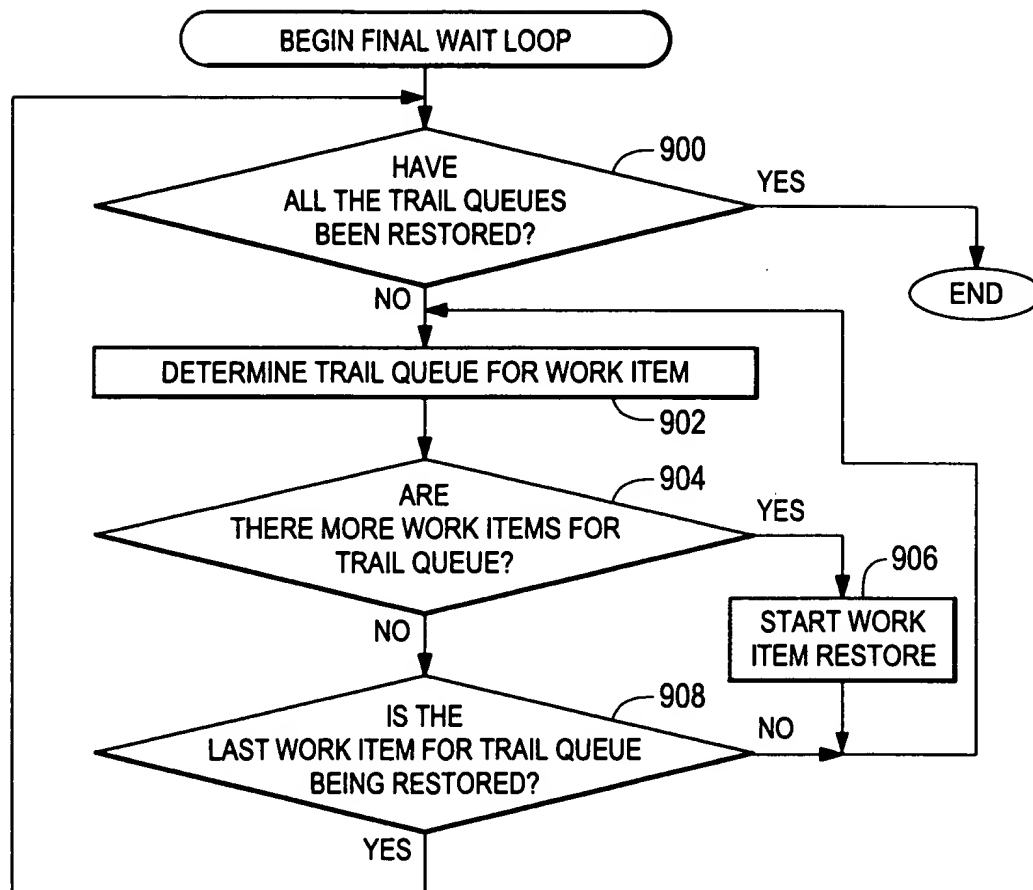




**FIG. 18**



**FIG. 19**



**FIG. 20**